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<b>From:</b>	Robert D. Atkins	602-229-5690	602-229-5311
<b>Re:</b>	USSN: 10/828,773		

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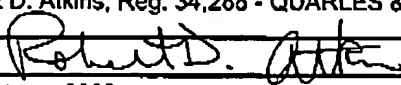
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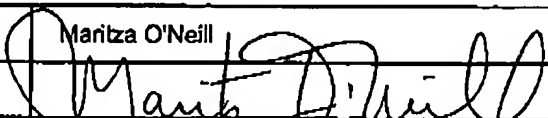
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<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/828,773	
	Filing Date	April 20, 2004	
	First Named Inventor	Badredin Fatemizadeh	
	Art Unit	2826	
	Examiner Name	Fetsum Abraham	
Total Number of Pages In This Submission	2	Attorney Docket Number	112518.00005

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Remarks <div style="text-align: center; border: 1px solid black; padding: 5px;"> <b>RESPONSE TO RESTRICTIONS/ELECTION REQUIREMENT</b> </div>		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Robert D. Atkins, Reg. 34,288 - QUARLES & BRADY STREICH LANG LLP
Signature	
Date	April 6, 2005

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Typed or printed name	Maritza O'Neill
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APR 06 2005

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Fatemizadeh et al.	Customer No.:	26707
Serial No.:	10/828,773	Confirmation No.:	5237
Filed:	April 20, 2004	Examiner:	Fetsum Abraham
For:	VERTICAL POWER JFET WITH LOW ON-RESISTANCE FOR HIGH VOLTAGE APPLICATIONS	Art Unit:	2826

## RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Restriction Requirement mailed March 22, 2005, Applicant hereby elects, without traverse, the claims classified as Group II in the Office Action. Namely, Applicant elects Claims 6-32 drawn to a transistor, classified in class 257, subclass 137.

Applicant hereby withdraws Claims 1-5 from further consideration in this Office Action, but reserves the right to prosecute these claims in one or more divisional applications, which may be filed before the close of prosecution of the instant application.

Respectfully submitted,  
QUARLES & BRADY STREICH LANG LLP

April 6, 2005

By: Robert D. Atkins  
Robert D. Atkins  
Reg. No. 34,288

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